

MiniApp One

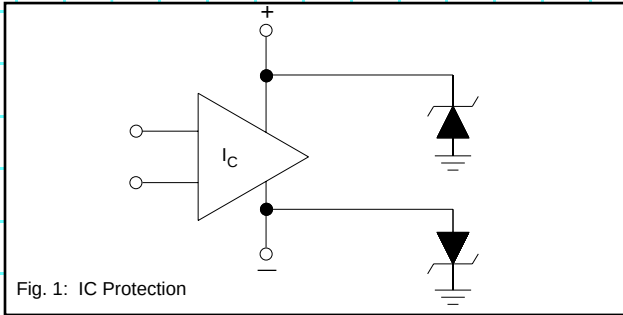


Fig. 1: IC Protection

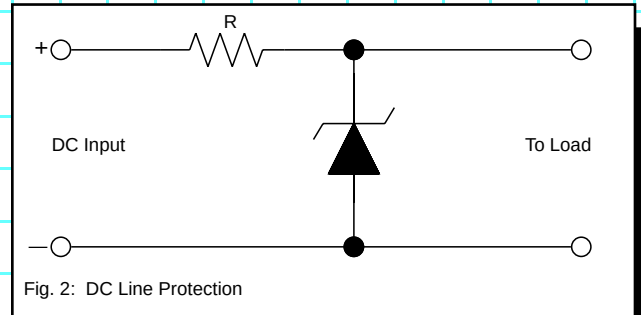


Fig. 2: DC Line Protection

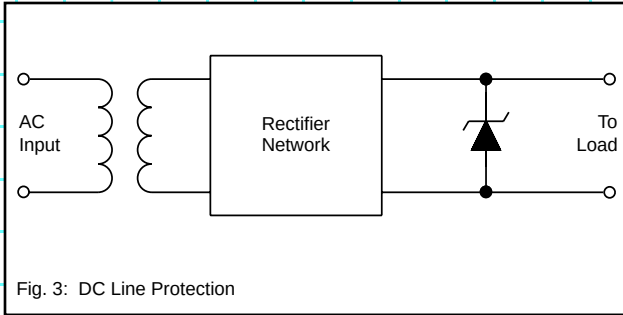


Fig. 3: DC Line Protection

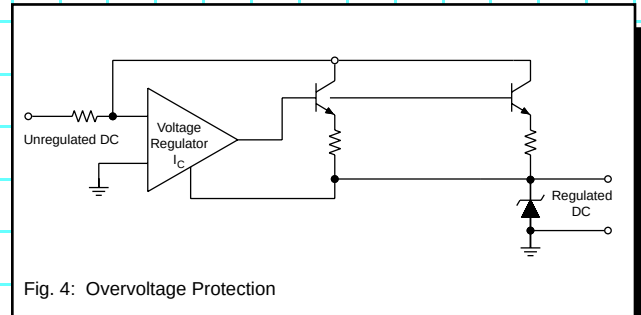


Fig. 4: Overvoltage Protection

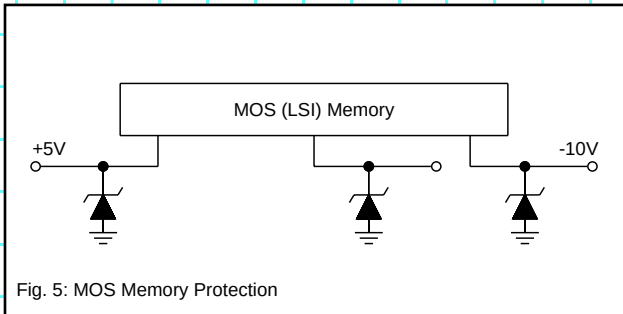


Fig. 5: MOS Memory Protection

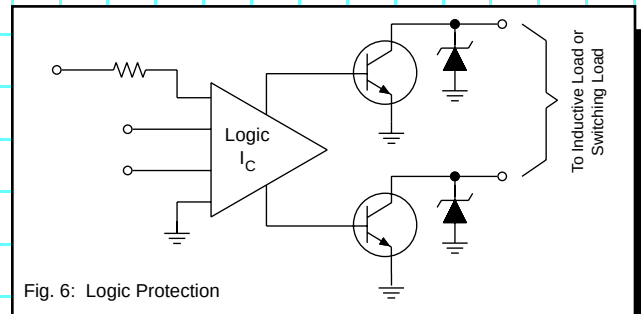


Fig. 6: Logic Protection

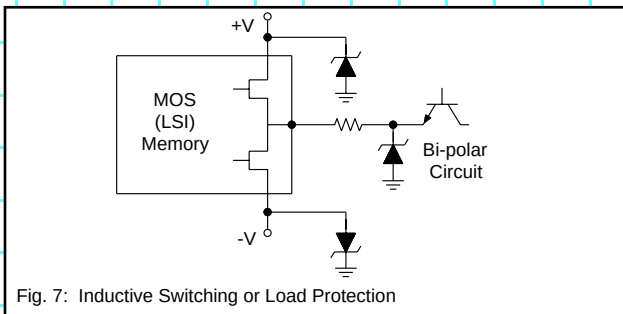


Fig. 7: Inductive Switching or Load Protection

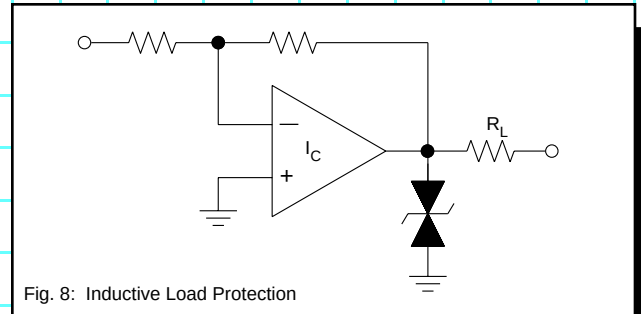


Fig. 8: Inductive Load Protection

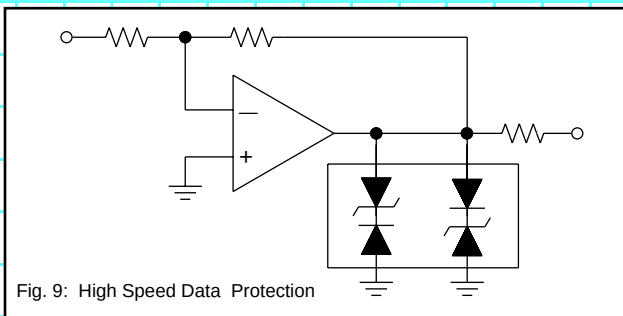


Fig. 9: High Speed Data Protection

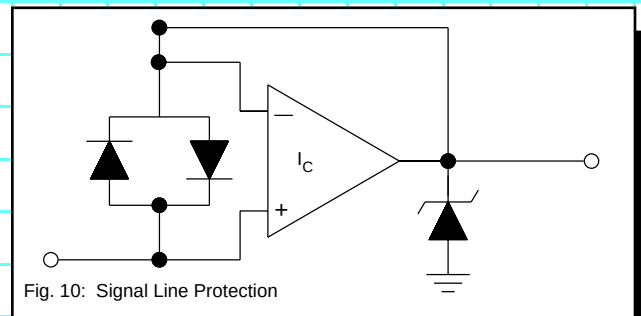
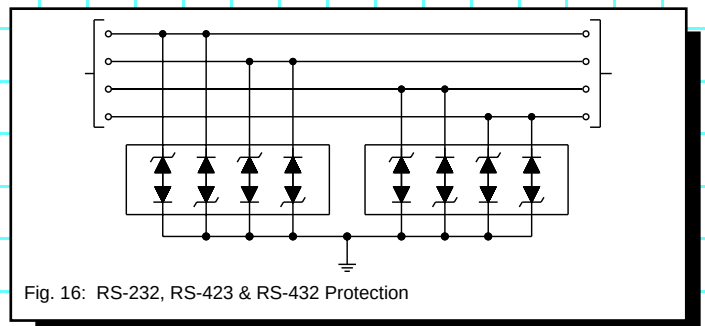
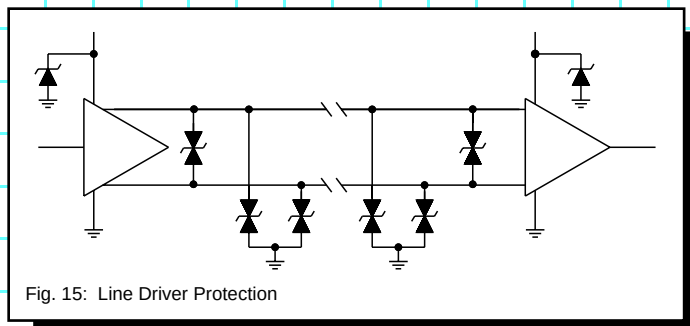
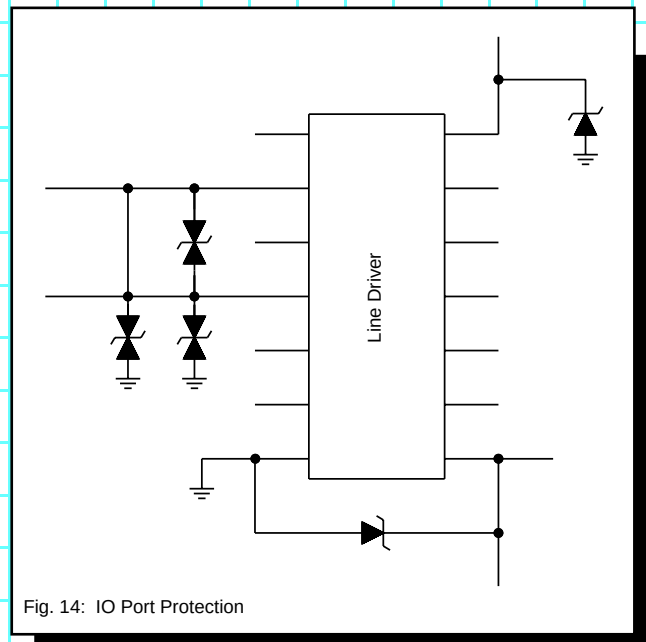
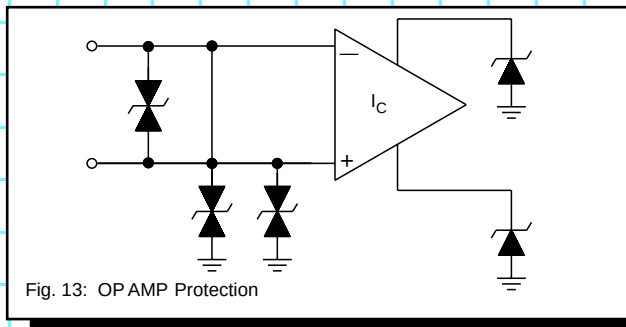
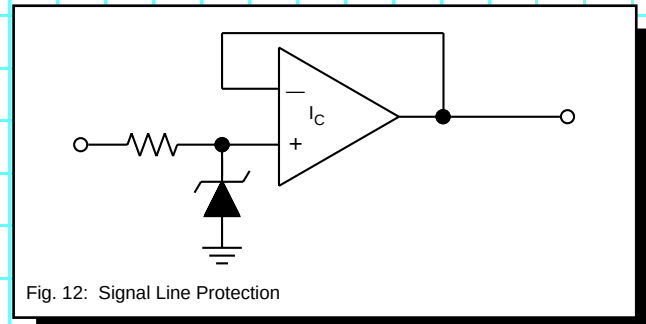
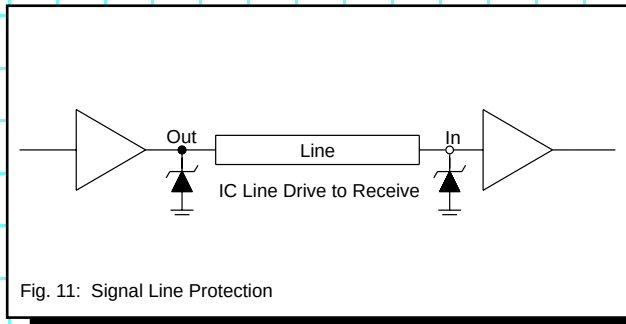


Fig. 10: Signal Line Protection

MiniApp Two



MiniApp Three

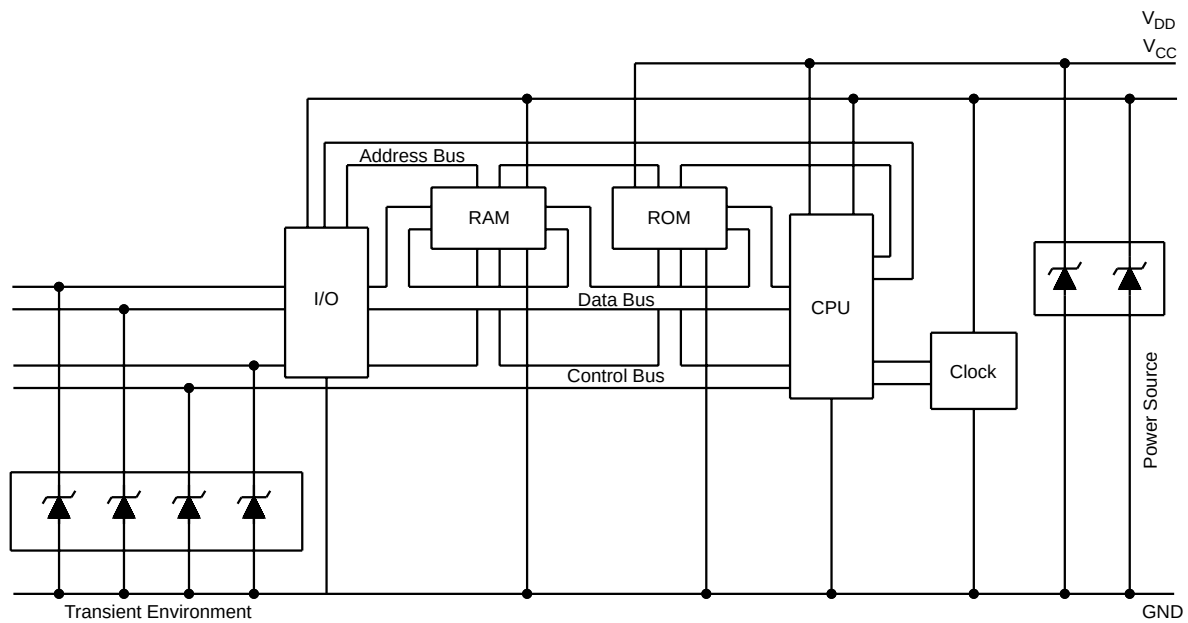


Fig. 17: Typical Microprocessor Protection

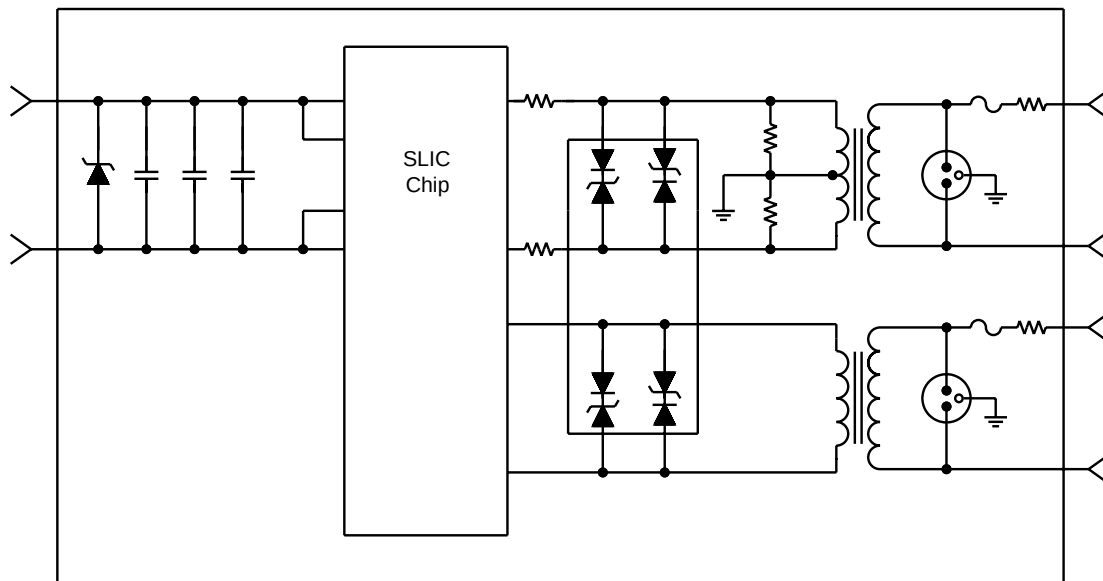


Fig. 18: SLIC Protection